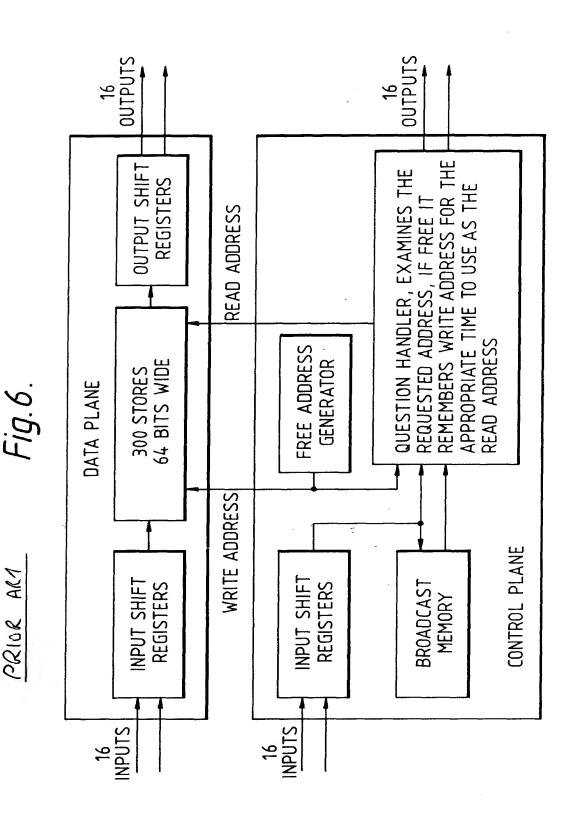


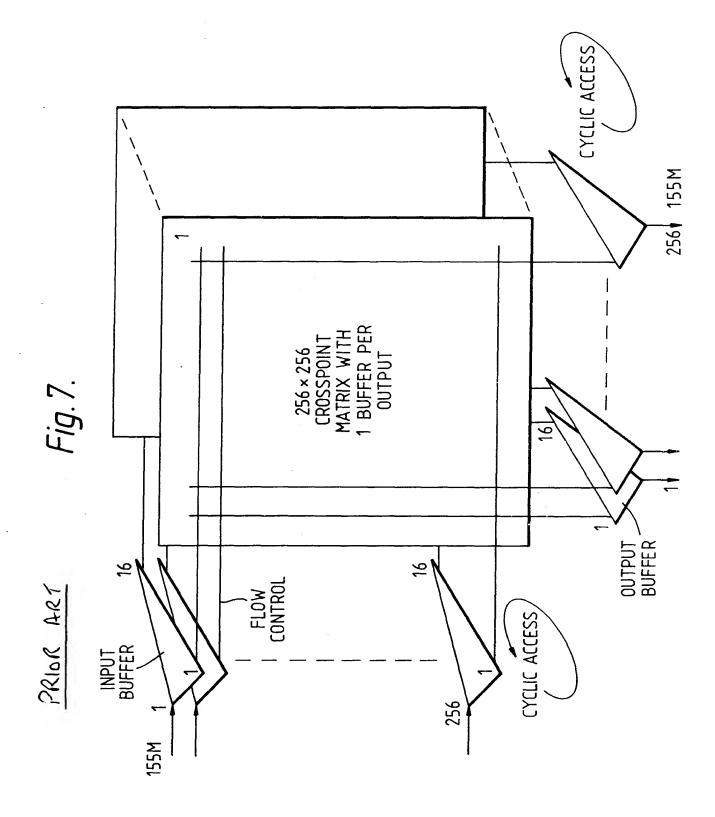
3/19 **Fig.4**. PRIOR ART PORT X CENTRAL SWITCH PORT Y IS THE BUFFER FOR PORT Y EMPTY? N₀ BUFFER OCCUPIED NEXT CENTRAL SWITCH IS THE BUFFER FOR PORT Y EMPTY? YES BUFFER EMPTY CELL CONTENTS) FIXED DELAY) CELL CONTENTS PRIOR ART Fig.5. PORT X CENTRAL SWITCH PORT Y HAVE YOU GOT SPACE FOR MULTI CAST CELL? NO SPACE IN USE ALREADY **NEXT CENTRAL SWITCH** HAVE YOU GOT SPACE FOR MULTI CAST CELL? YES SPACE AVAILABLE **CELL CONTENTS** FIXED DELAYS **CELL CONTENTS** PORT Y

> DELAY ADDED IF NEEDED BEFORE ENTERING THE OUTPUT QUEUE

PORT Z

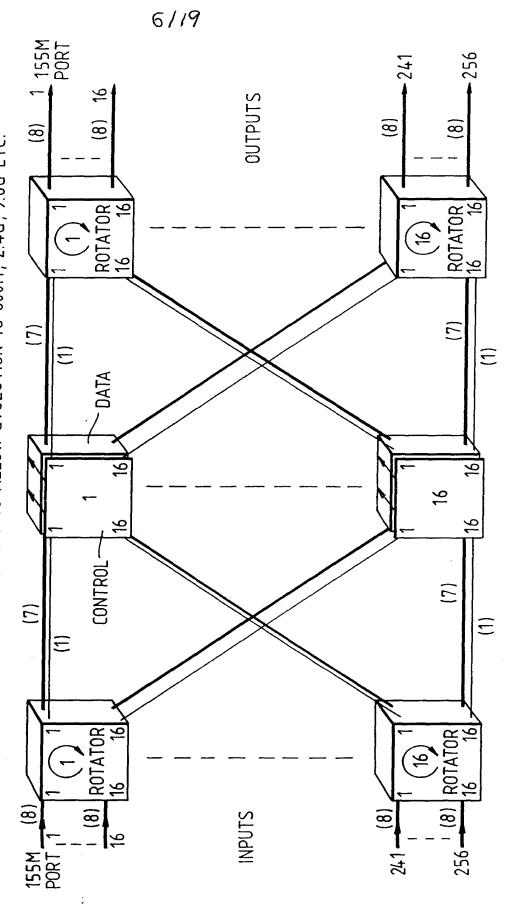
CELL CONTENTS

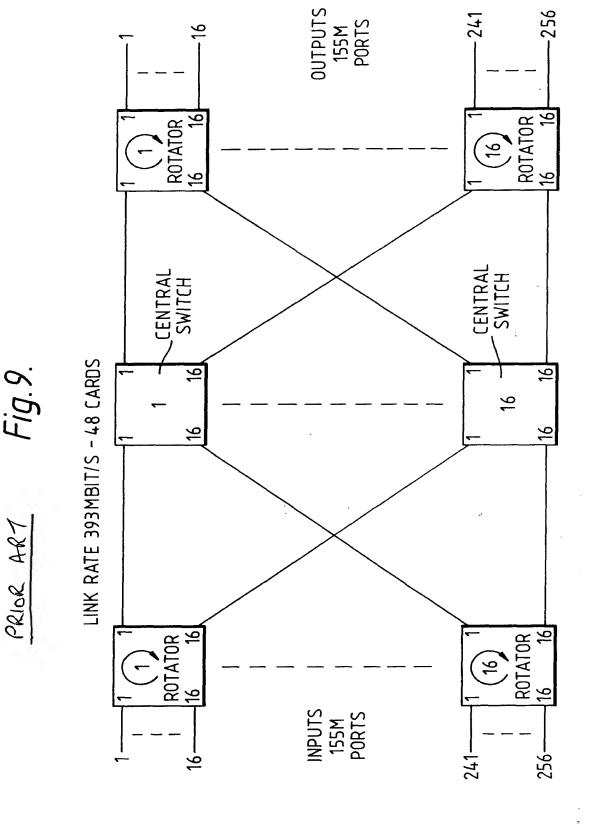




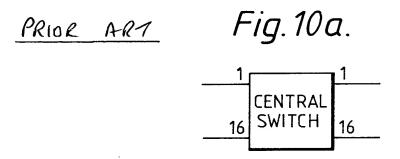
PRIOR ART FIG. 8.

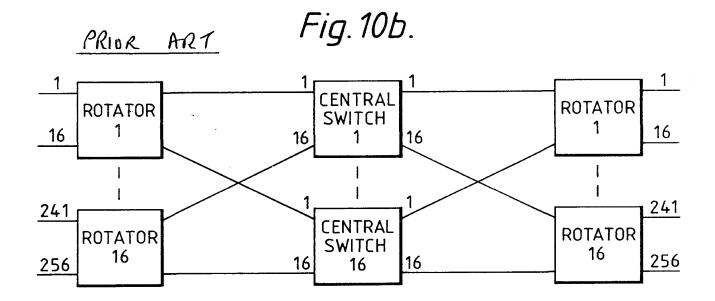
NOTE : ANY NUMBER OF INPUT/OUTPUT "PORTS" CAN BE CONCATENATED WITH GUARANTEED CELL SEQUENCE INTEGRITY TO ALLOW EVOLUTION TO 600M, 2.4G, 9.6G ETC.

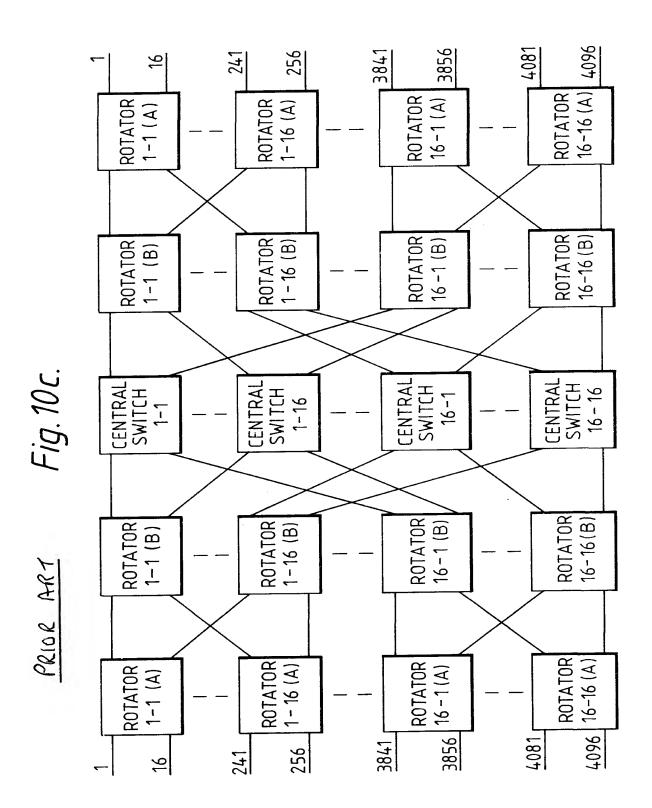


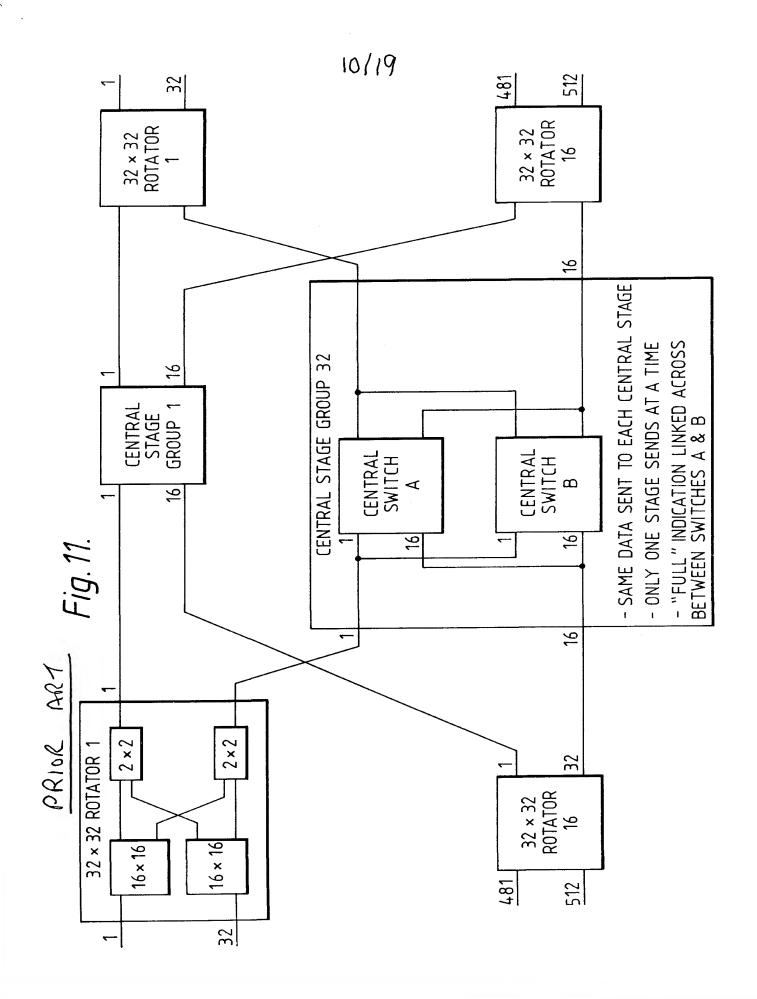


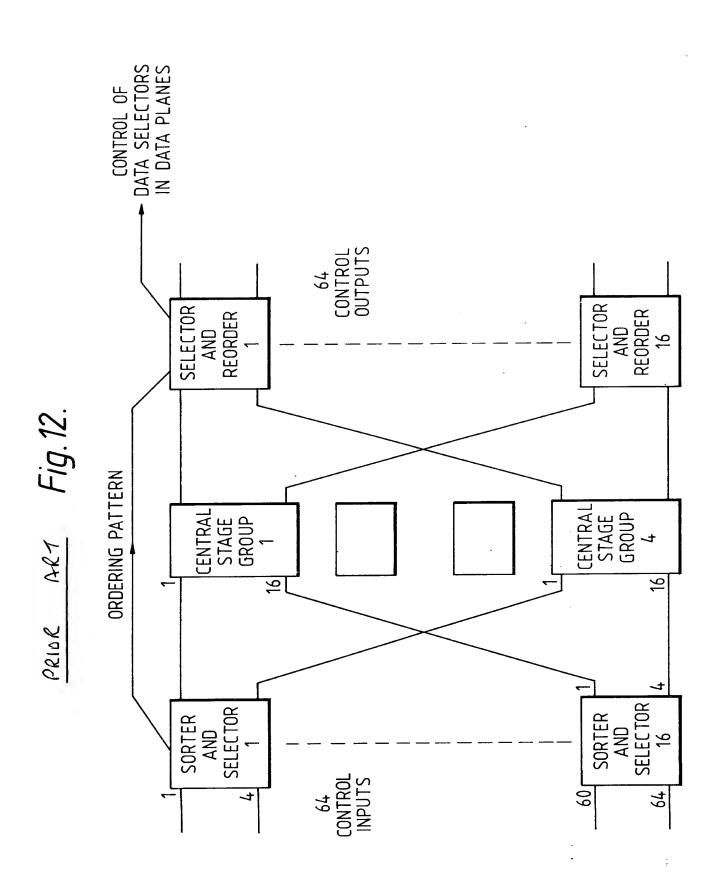
NOTE: OPTICAL ROTATORS HALVE THE NUMBER OF ROTATOR CARDS BY COMBINING Rx AND Tx FUNCTIONS.



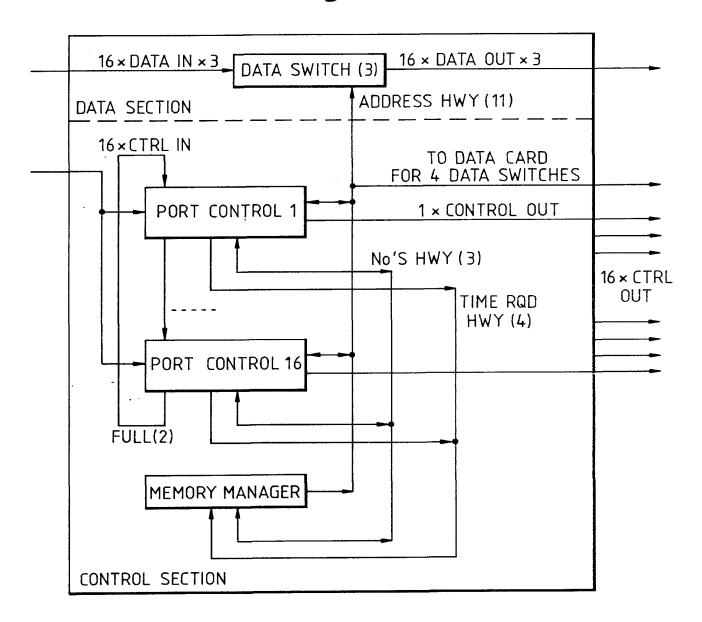




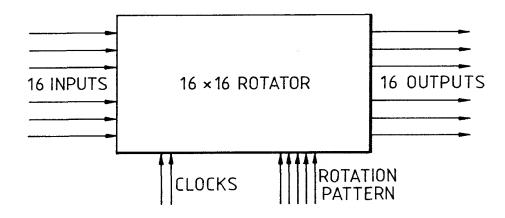




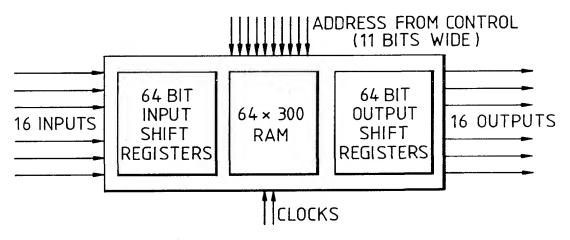
PRIDE ART Fig. 13.



PRIOR ART Fig. 14.



PRIOR ART Fig. 15.



PRIOR ART Fig. 16.

No'S (3)

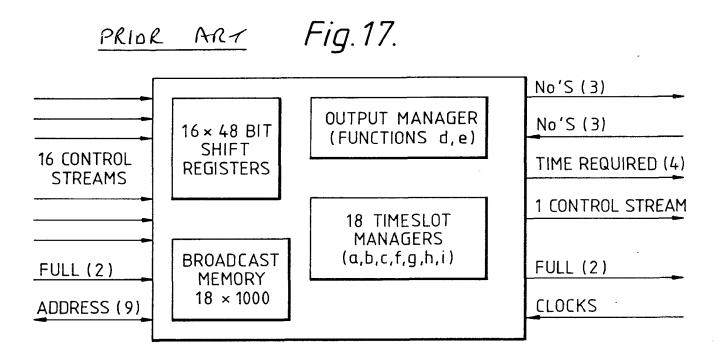
RETURN
IN TIME
REQUIRED (4)

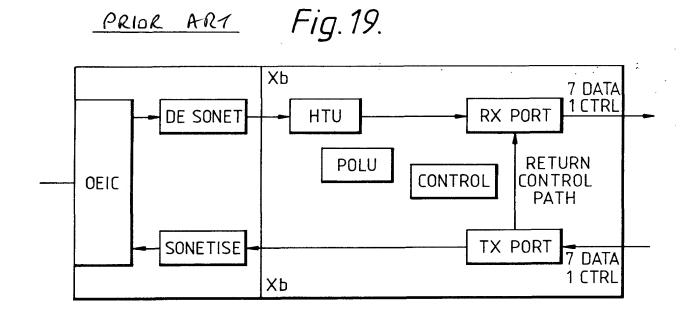
FREE
POOL

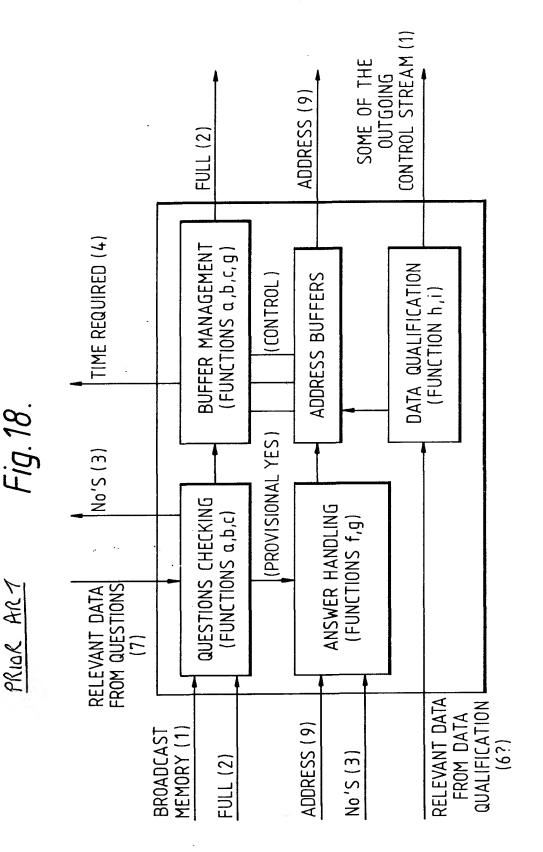
ADDRESS
MANAGER

No'S (3)

FREE ADDRESS (9)

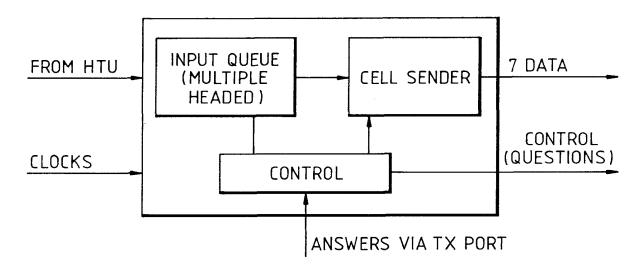




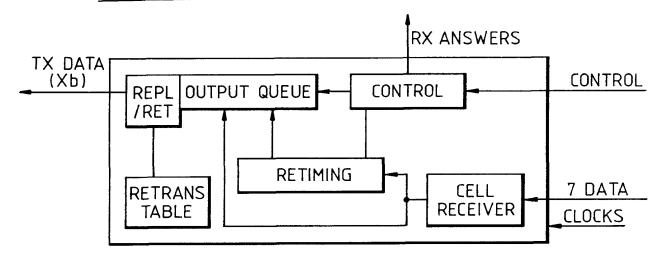


PRIOR ART

PRIDE ART Fig. 20.



PRIOR ART Fig. 21.



0 PORT 1 PORT 2 PORT 3 PORT 4 PORT 5 ETC.	STITS TIMOS OF TIMOS SON TO A SOLD TIMOS TO STITS TO STIT
PORT 0	1
	PORT 0 PORT 1 PORT 2 PORT 3 PORT 4 PORT 5 ETC.

PRIOR ART

Fig. 22B.

TOP RANK	MID RANK(S)	PORT 5 MAIN RANK
		PORT 5
		PORT 4
		PORT 2 PORT 3 PORT 4
		PORT 2
	,	PORT 1
		PORT 0 PORT
FORWARD TRANSFER SPACE		AS ABOVE

ADDITIONAL STORAGE FOR MULTIPOINT CELLS

